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PATENT NUMBER and  
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM 10051916	FILING DATE 01/17/2002	CLASS 331	SUBCLASS 176	GAU 2817	EXAMINER Shrageon
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\*\*CONTINUING DATA VERIFIED:

MS NONE

\*\* FOREIGN APPLICATIONS VERIFIED:

REPUBLIC OF KOREA 2001-30522 05/31/2001

MS

PG-PUB	DO NOT PUBLISH <input type="checkbox"/>	RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiner's initials	<input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input checked="" type="checkbox"/> yes <input type="checkbox"/> no MS	ATTORNEY DOCKET NO 5649-947	
TITLE : Clock generation circuits and integrated circuit memory devices for controlling a clock period based on temperature and methods for using the same			
U.S.DEP'T. OF COMM./PAT & TM-PTO-435L(Rev. 12-94)			

NOTICE OF ALLOWANCE MAILED		Assistant Examiner	CLAIMS ALLOWED	
			Total Claims	Print Claim for O.G
ISSUE FEE		Primary Examiner	DRAWING	
Amount Due	Date Paid		Sheets Draw.	Figs. Draw.
<input type="checkbox"/> TERMINAL DISCLAIMER		Application Examiner		
PREPARED FOR ISSUE				
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